

FIG.1A

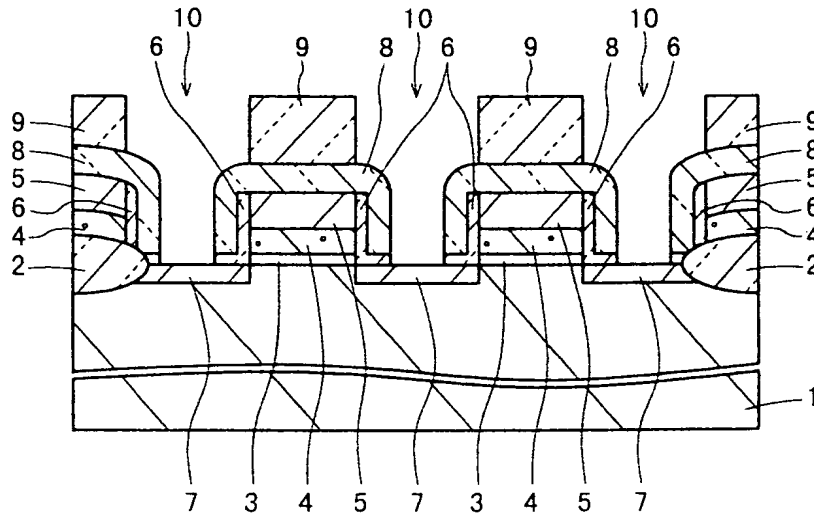


FIG.1B

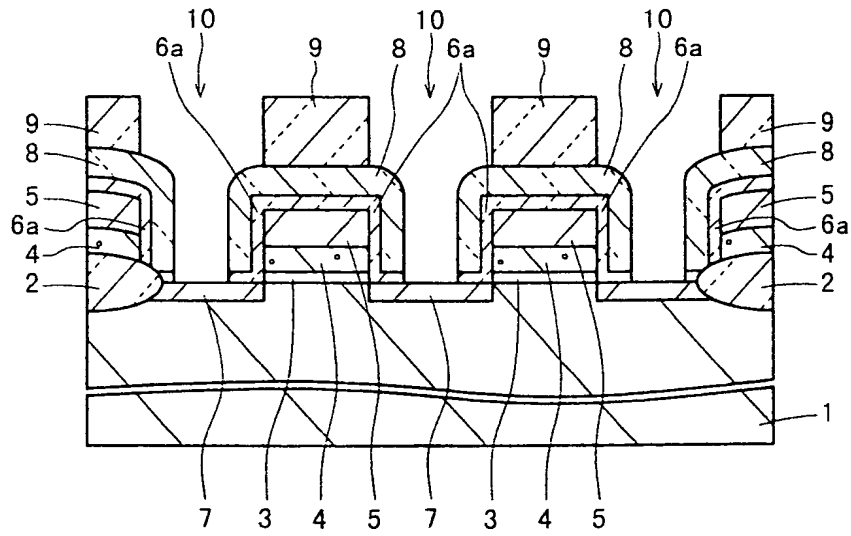


FIG.2

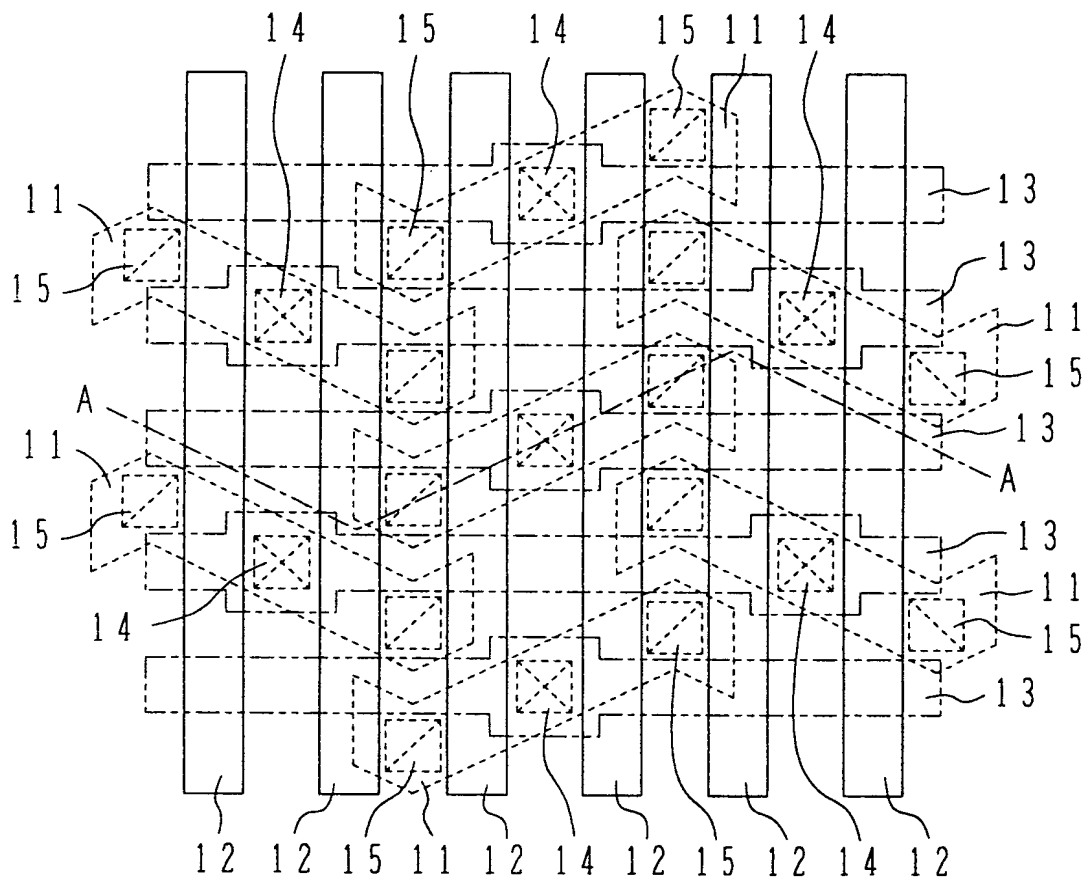


FIG. 3A

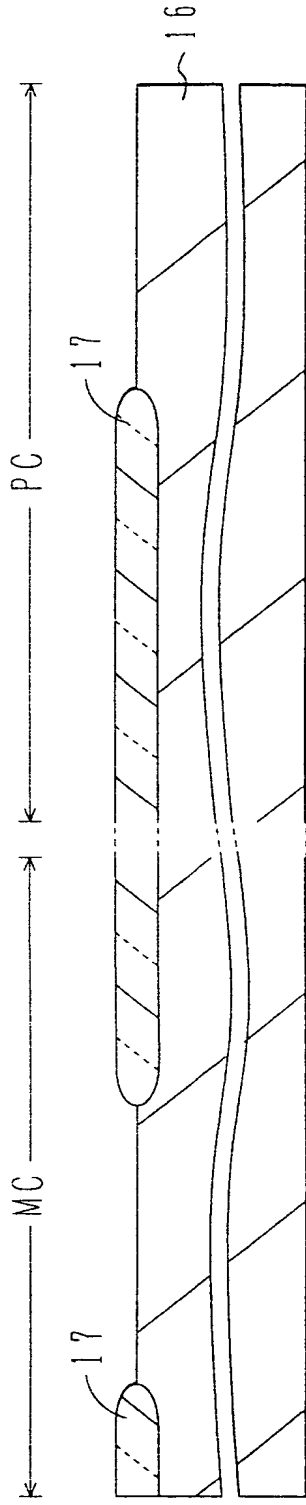


FIG. 3B

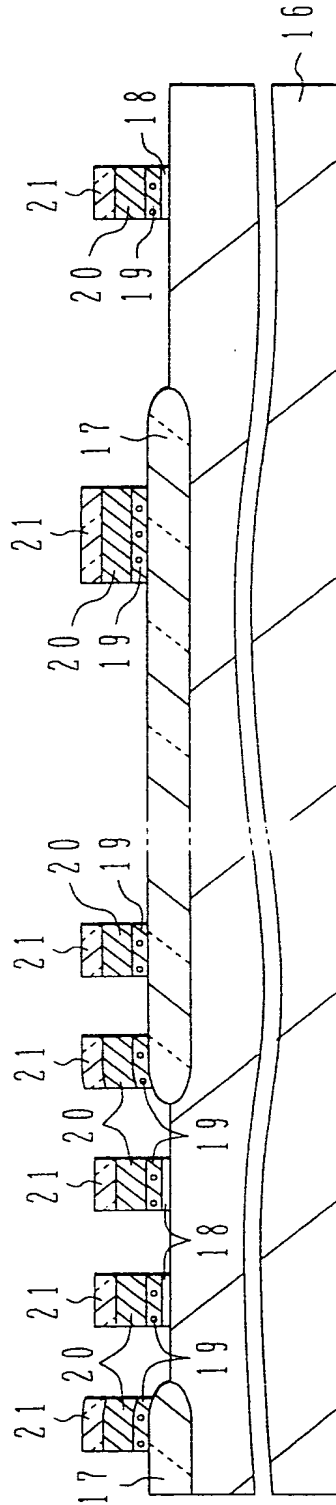


FIG. 4A

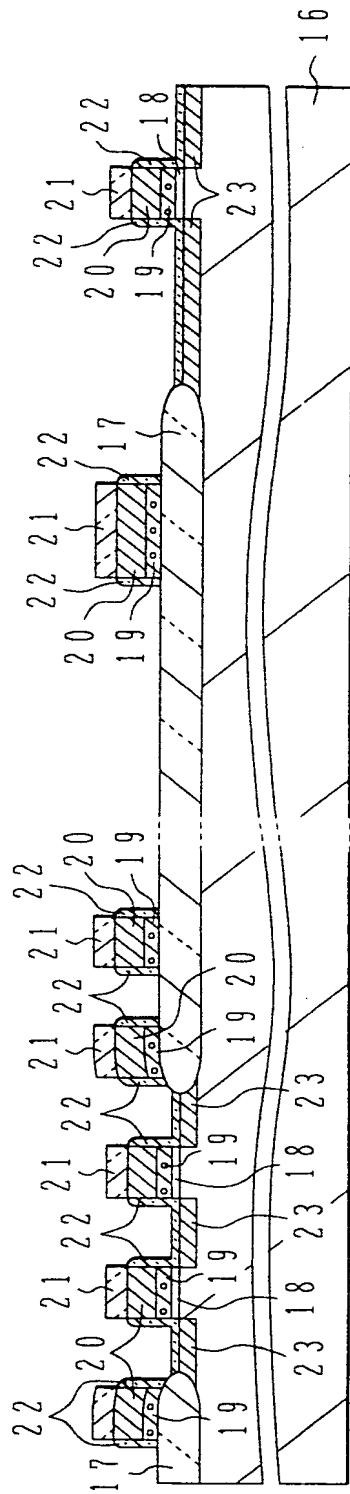
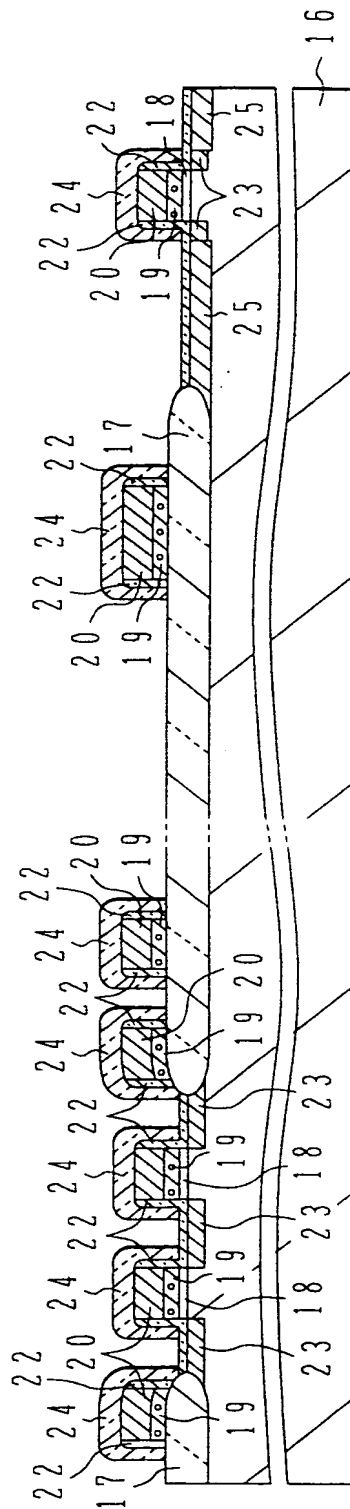


FIG. 4B



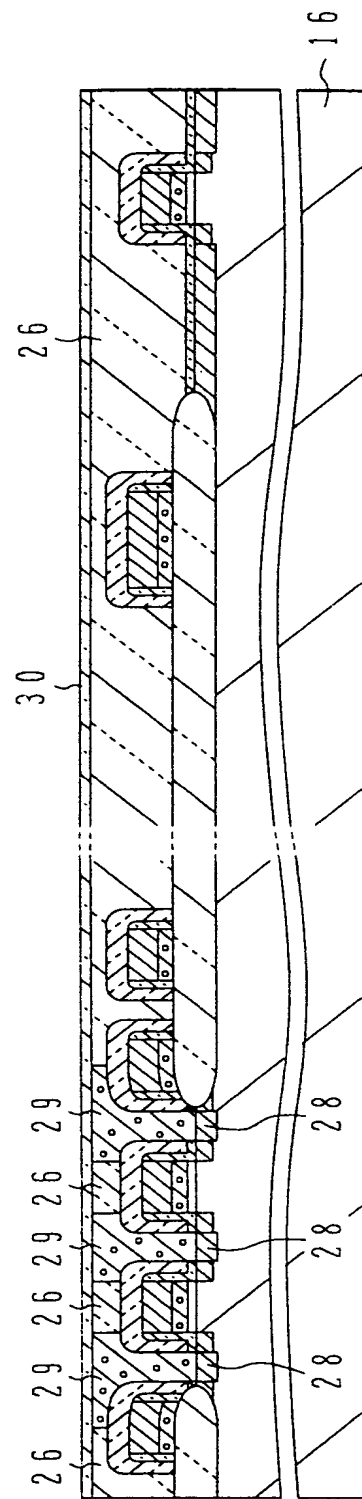
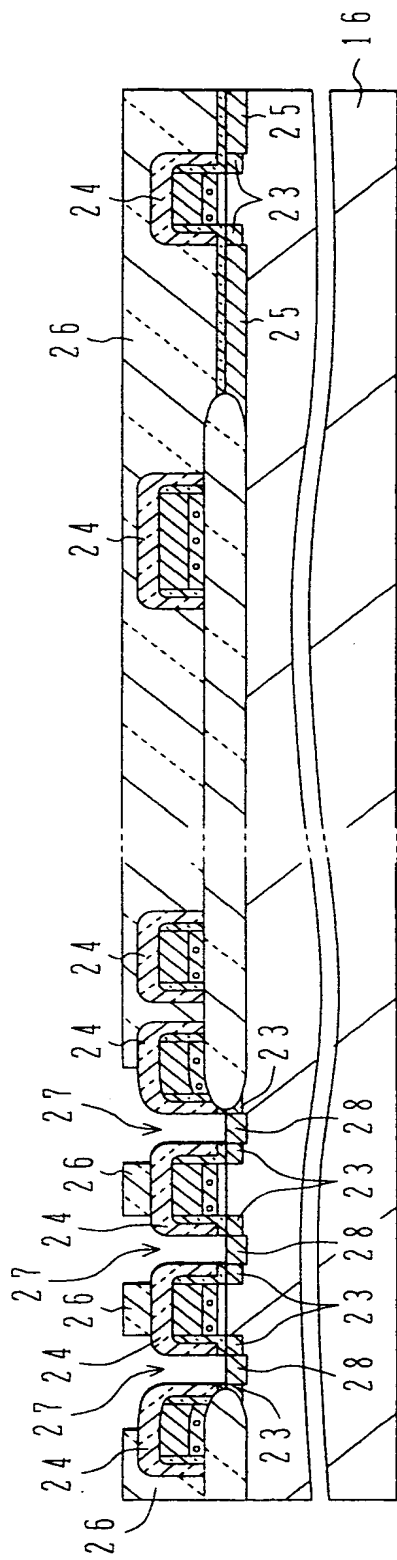


FIG. 6A

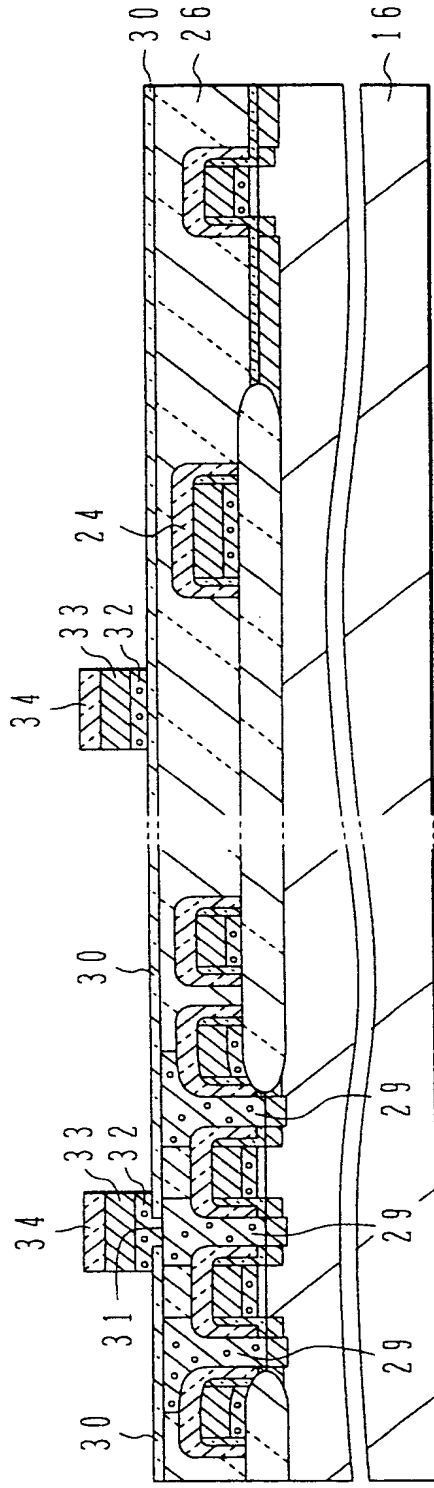


FIG. 6B

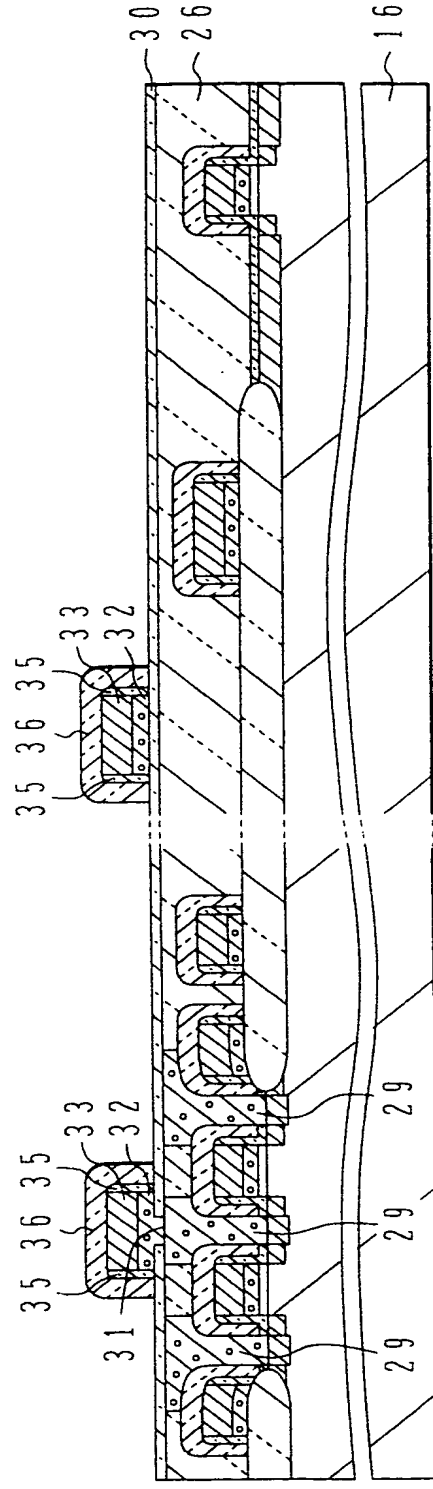


FIG. 7 is a cross-sectional view of a semiconductor device in accordance with the present invention. The device includes a substrate 16, a gate stack 26, and a gate electrode 30. The gate stack 26 is formed on the substrate 16, and the gate electrode 30 is formed on the gate stack 26. The gate electrode 30 is divided into a plurality of segments 31, 32, 33, 34, 35, 36, 37, and 38. The segments 31, 32, 33, 34, 35, 36, 37, and 38 are separated by insulating spacers 39. The segments 31, 32, 33, 34, 35, 36, 37, and 38 are formed of a conductive material, and the insulating spacers 39 are formed of an insulating material. The segments 31, 32, 33, 34, 35, 36, 37, and 38 are used to provide a gate voltage to the gate electrode 30, and the insulating spacers 39 are used to isolate the segments 31, 32, 33, 34, 35, 36, 37, and 38 from each other.

FIG. 7

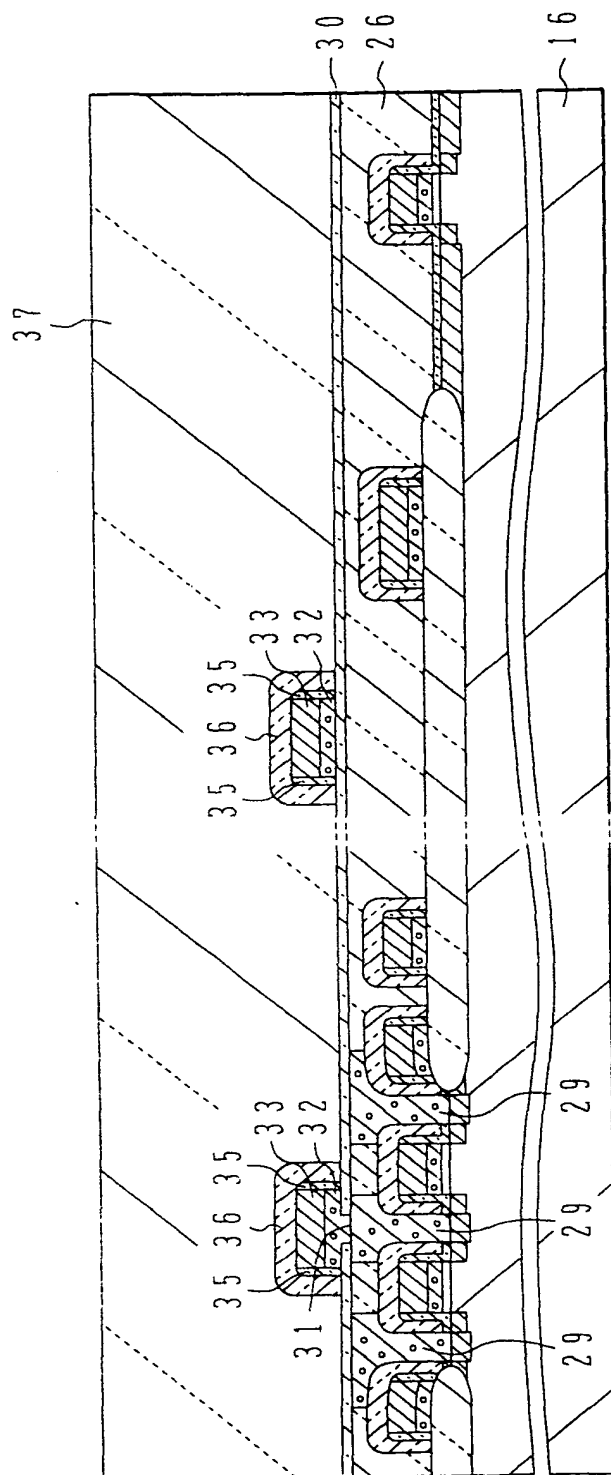


FIG.9

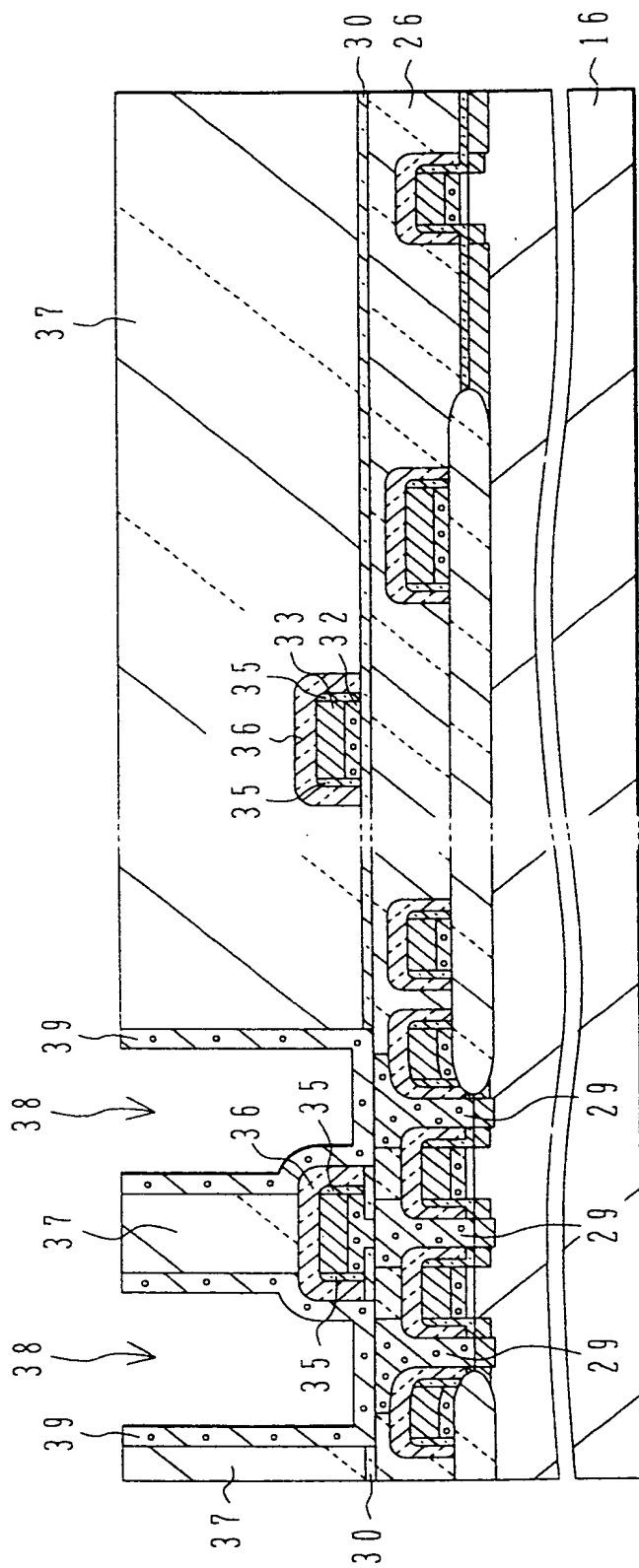


FIG.10

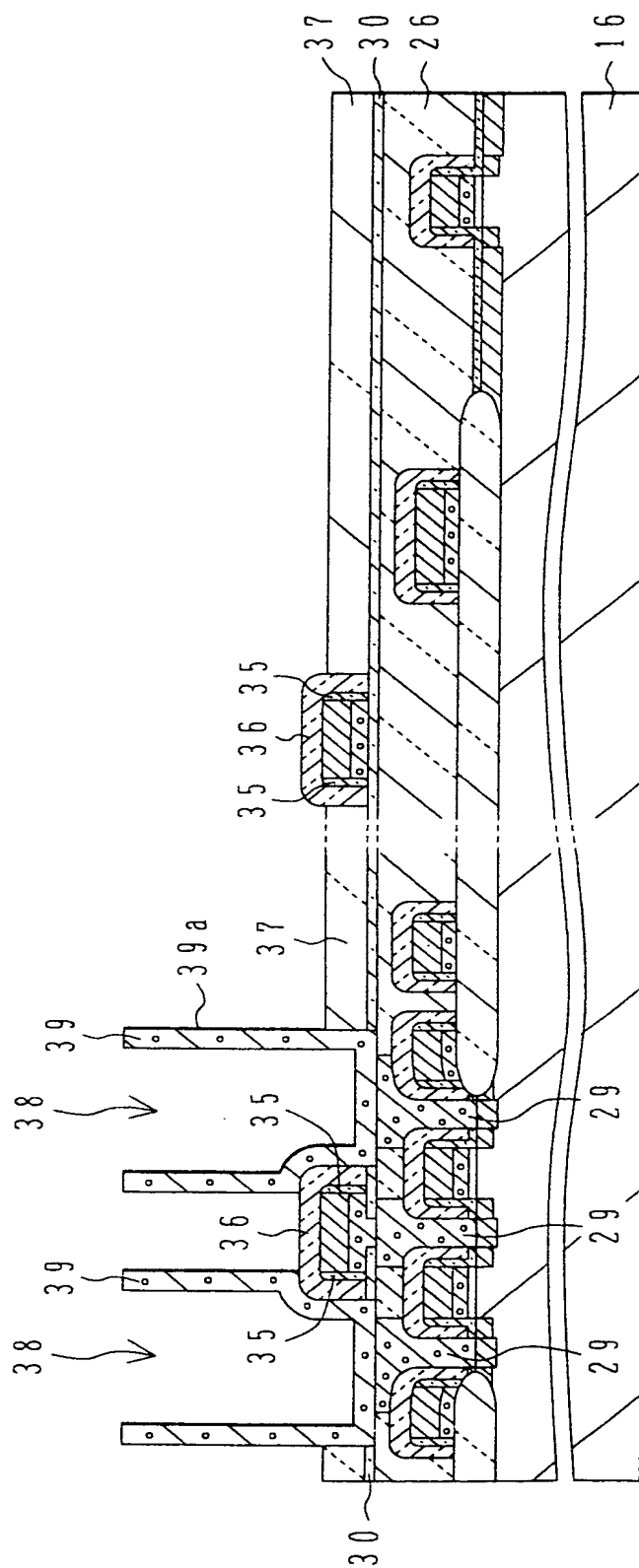


FIG.11

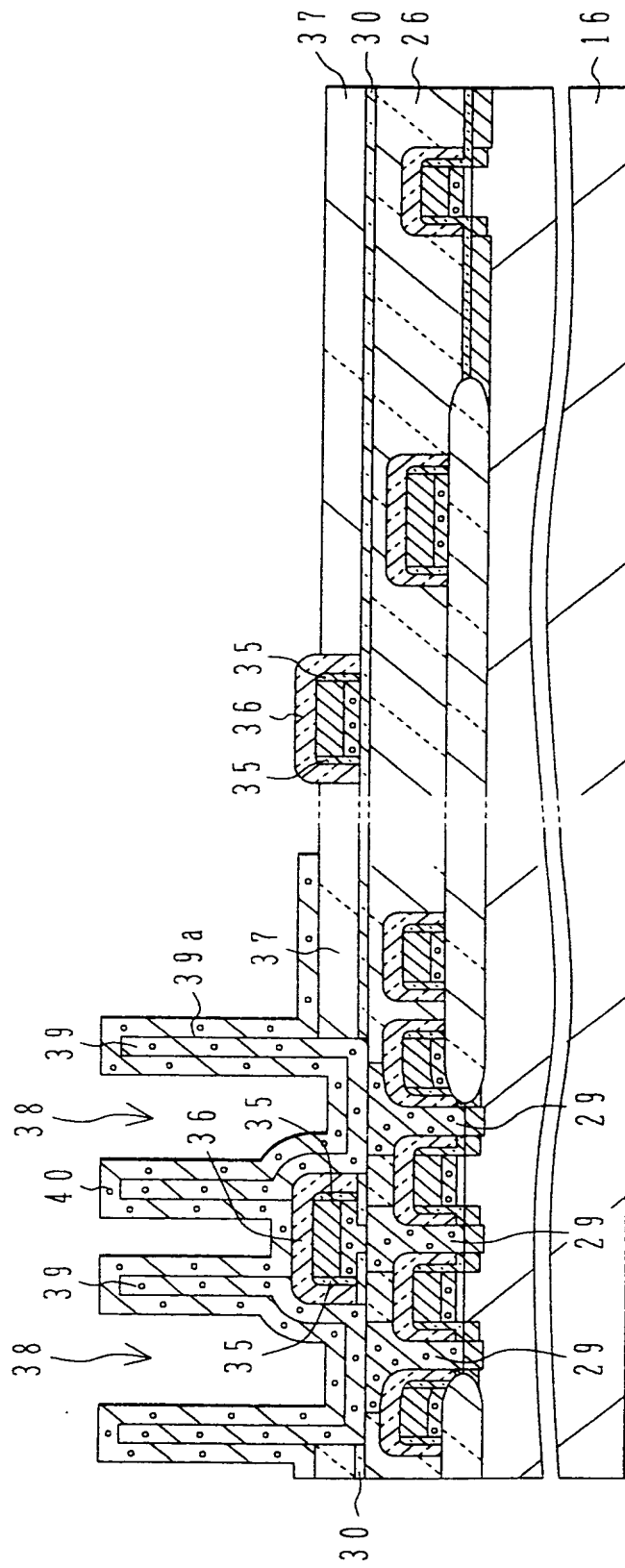


FIG.12

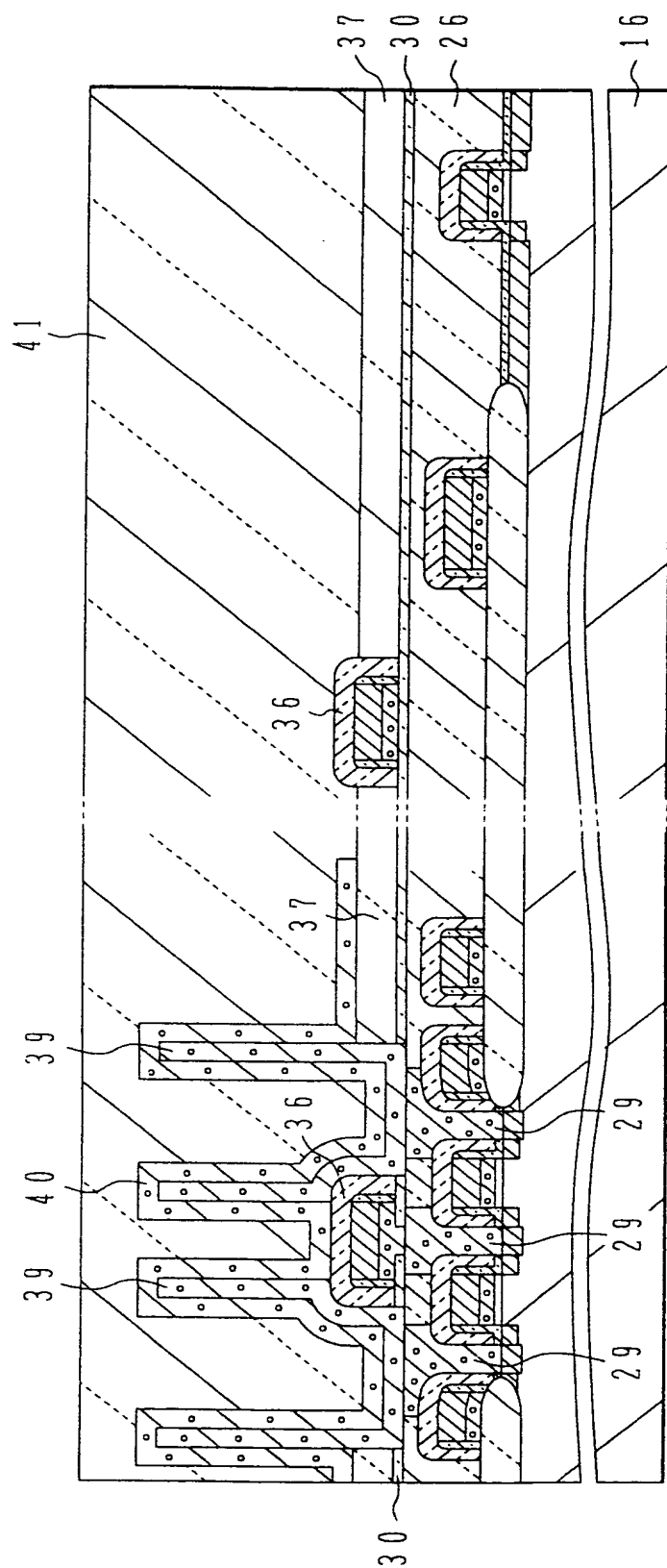


FIG. 14

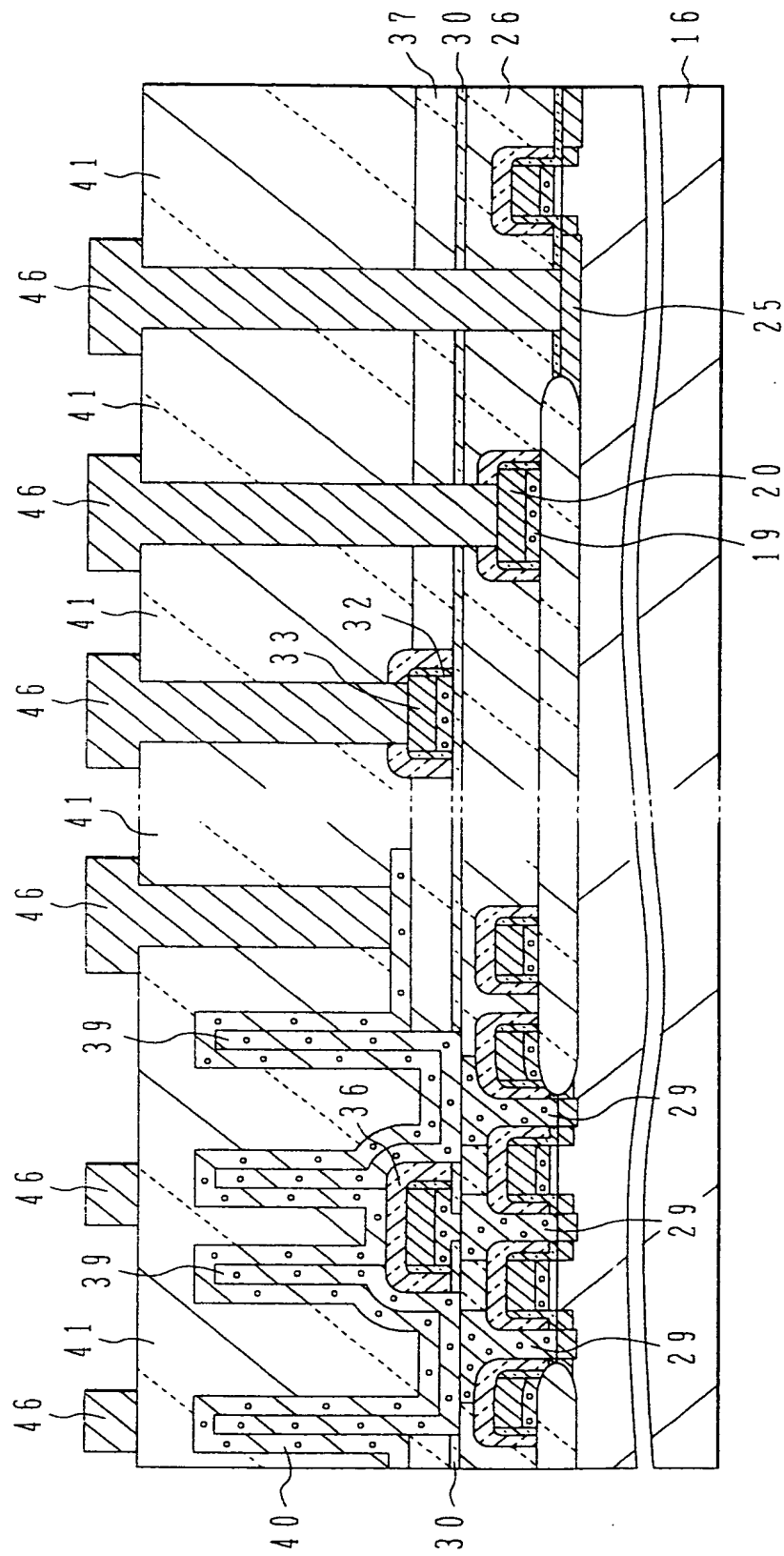


FIG. 15A

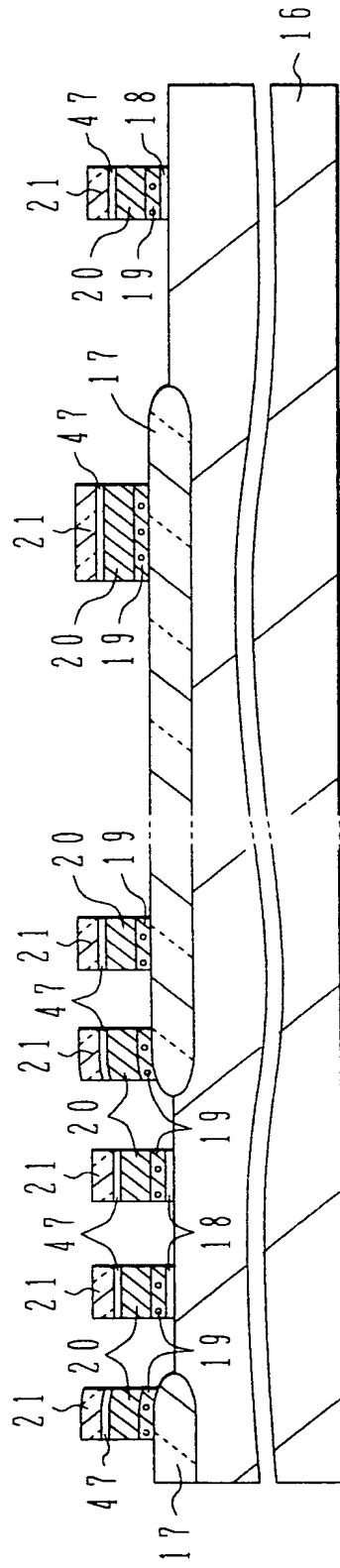


FIG. 15B

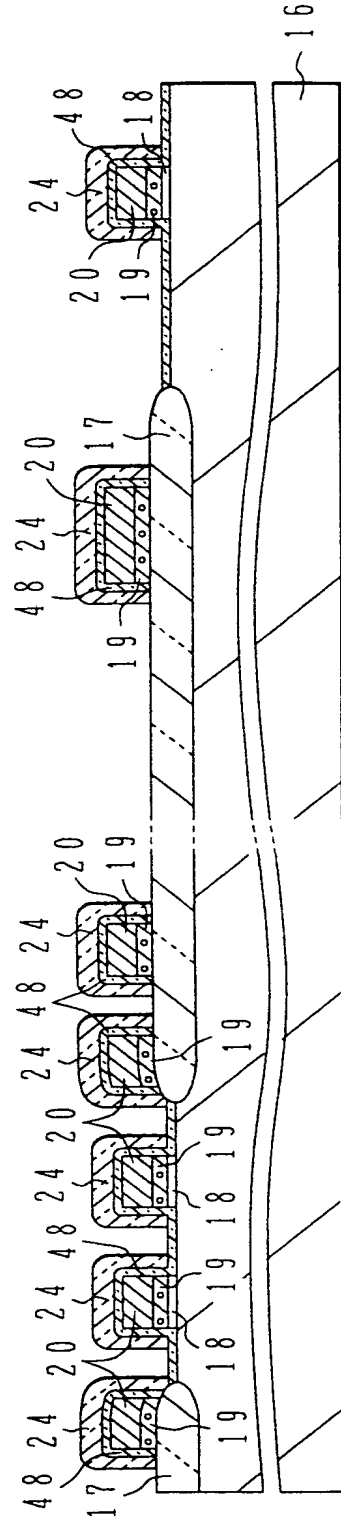


FIG.16A

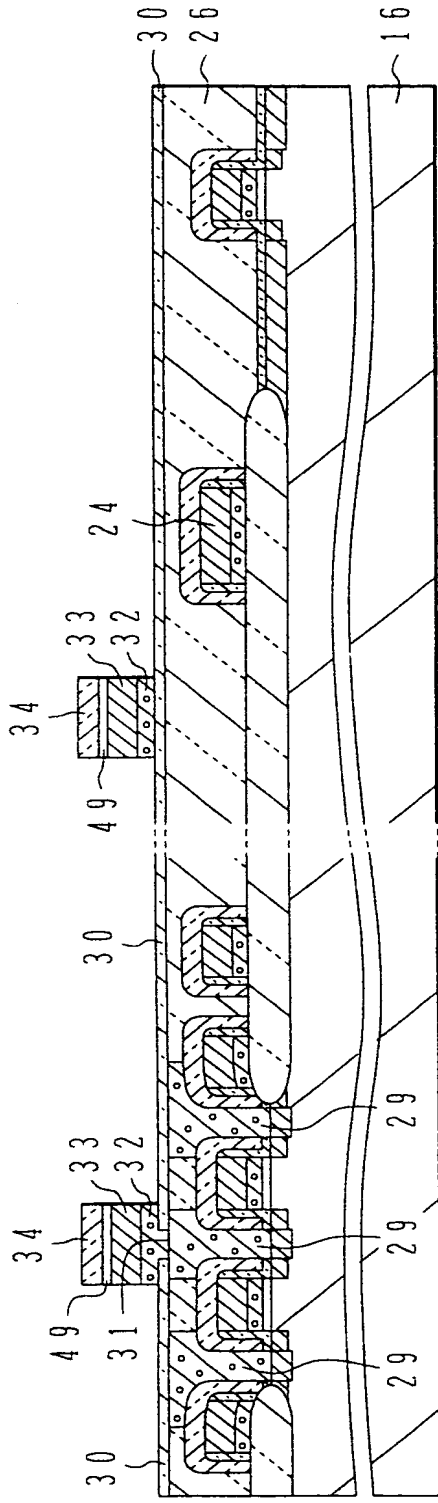


FIG.16B

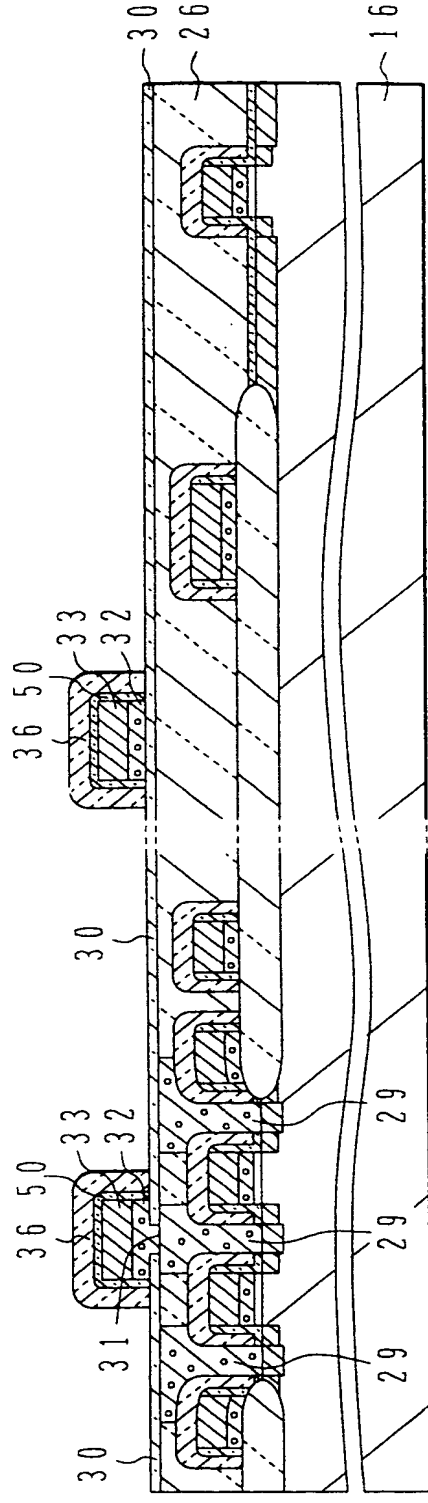


FIG. 17A

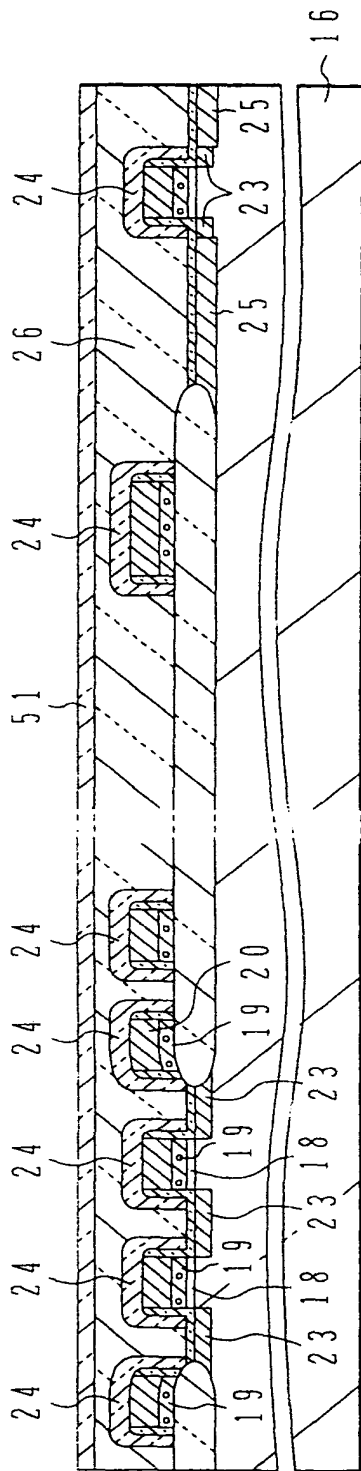


FIG. 17B

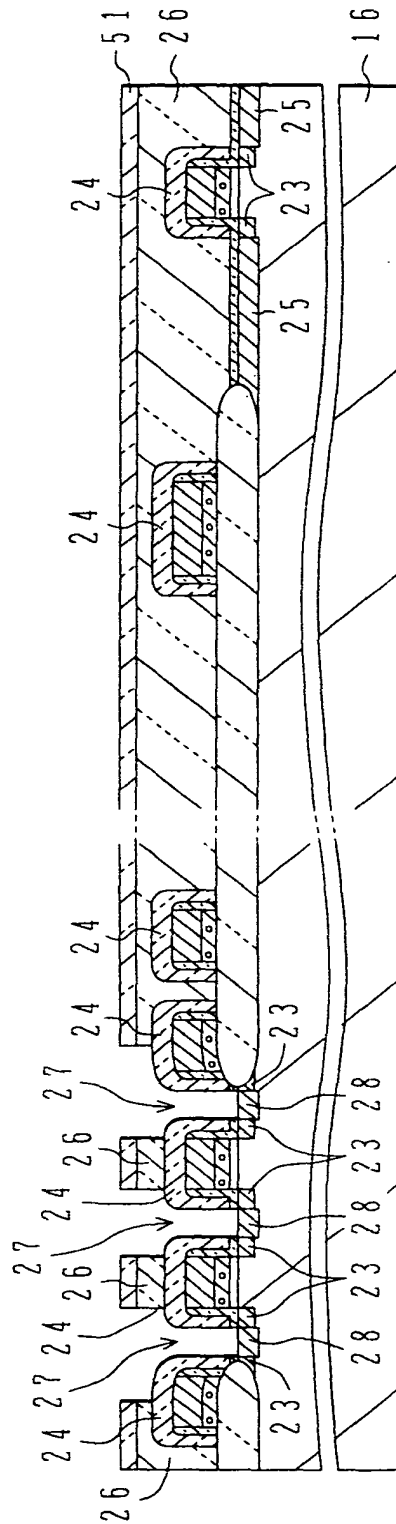


FIG.18A

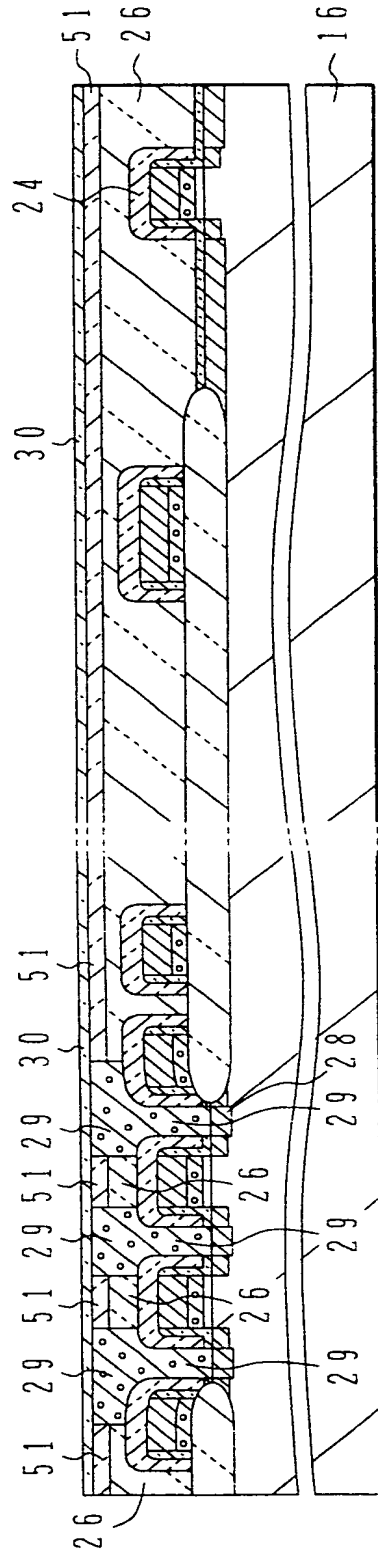


FIG.18B

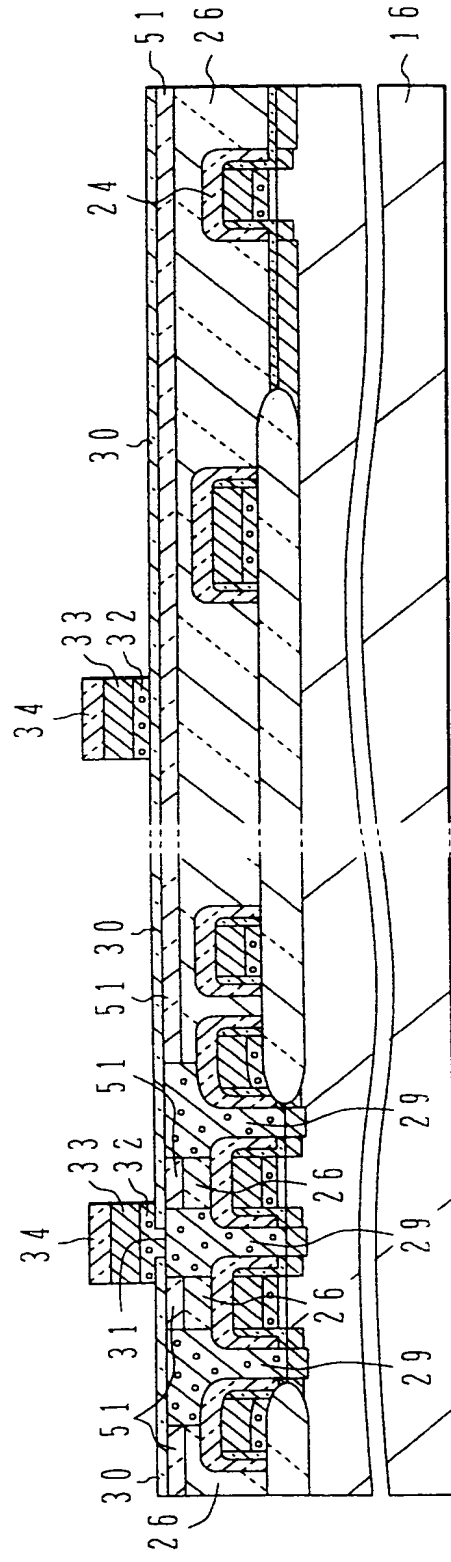


FIG. 20

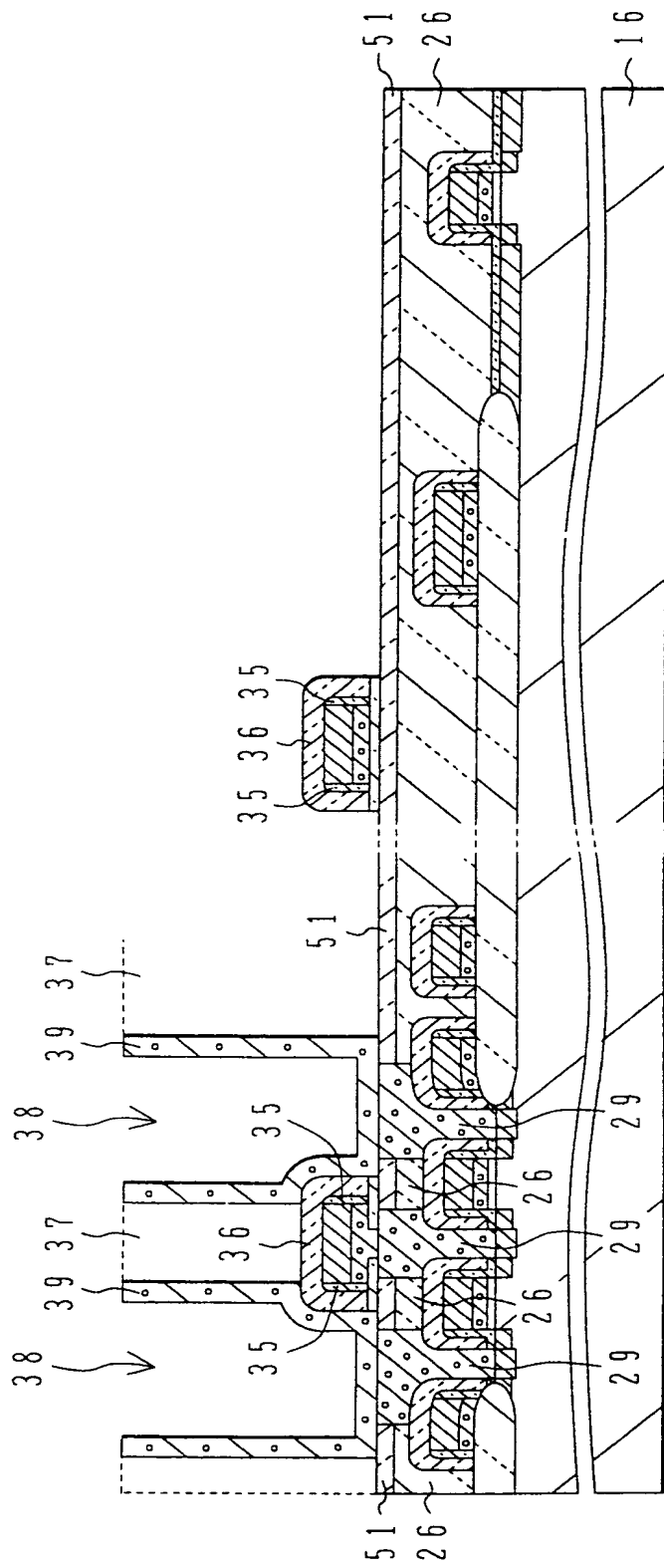


FIG.21

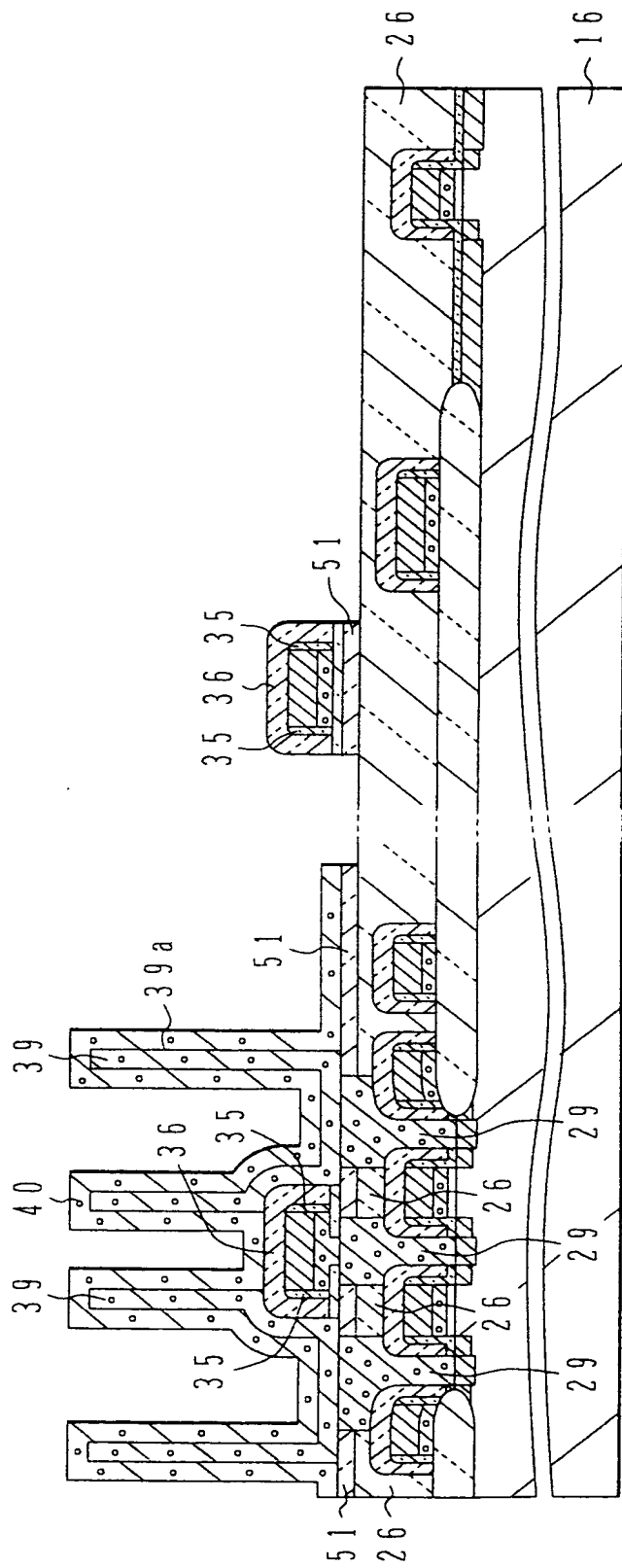


FIG.22

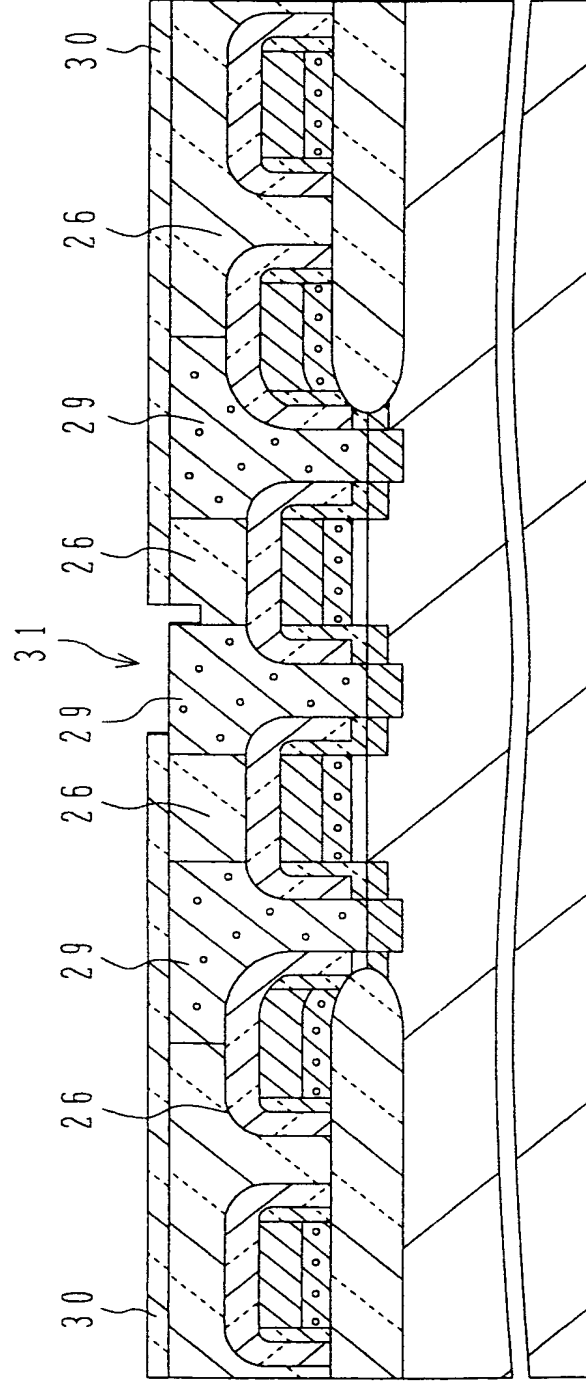


FIG. 23

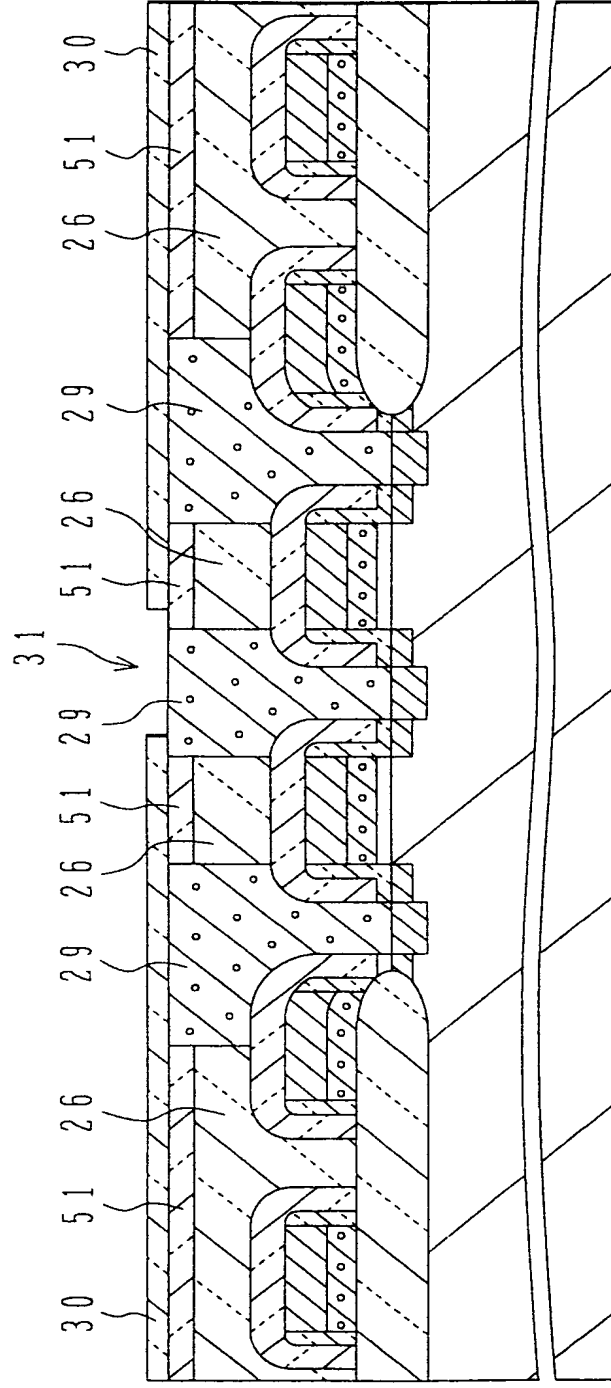


FIG. 24

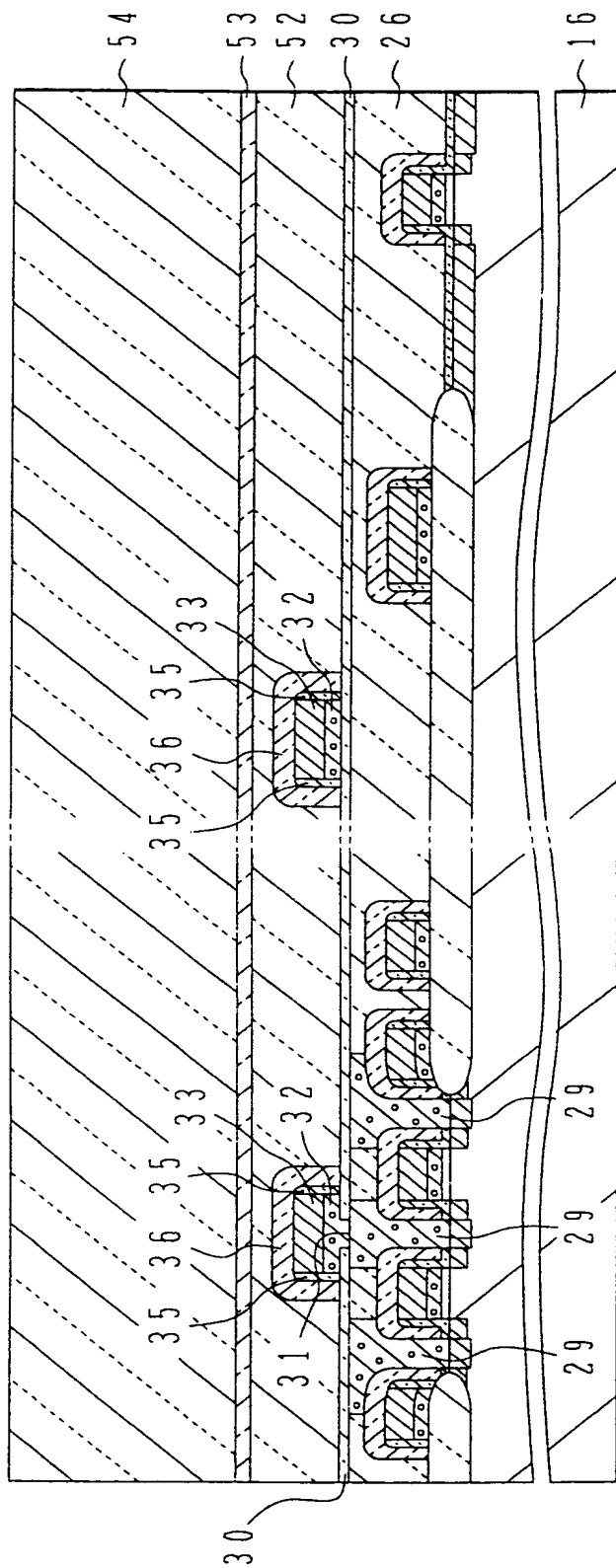


FIG. 25

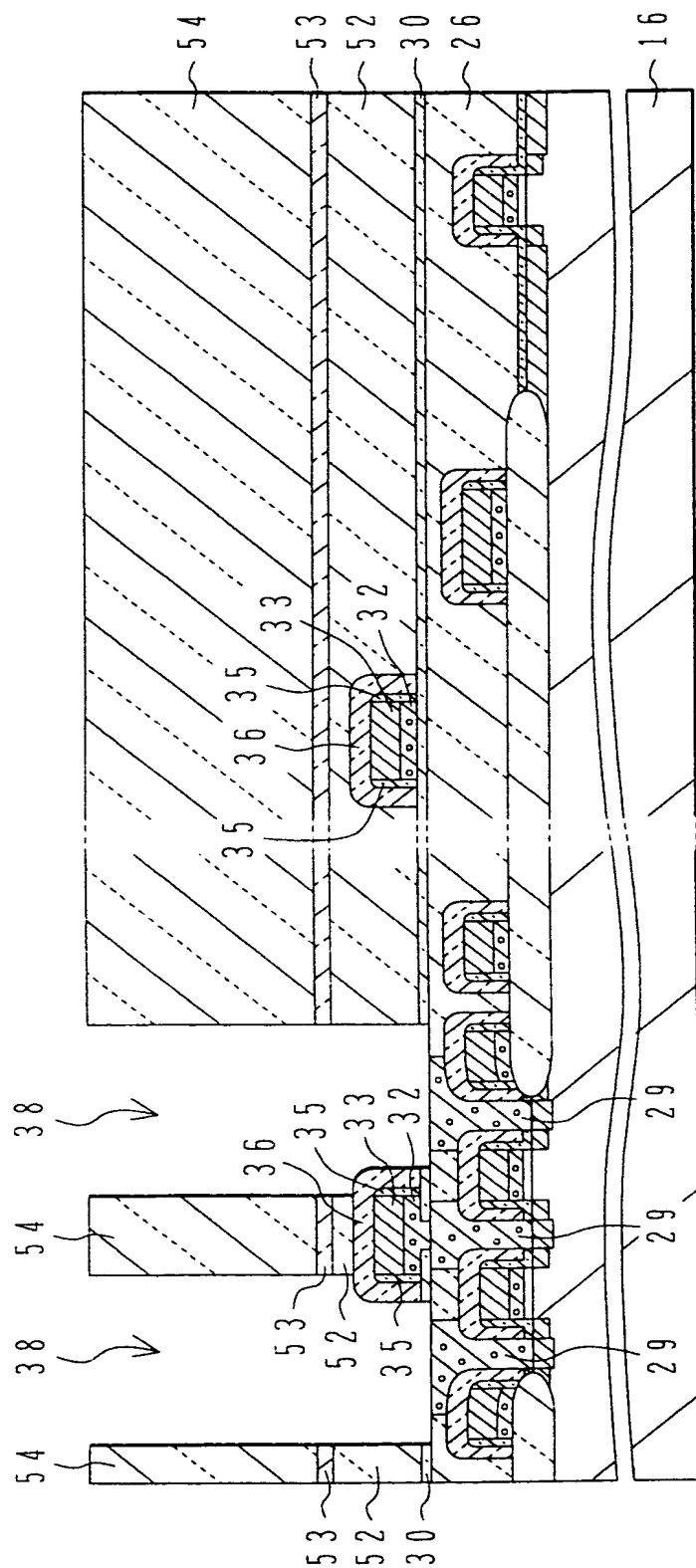


FIG. 26

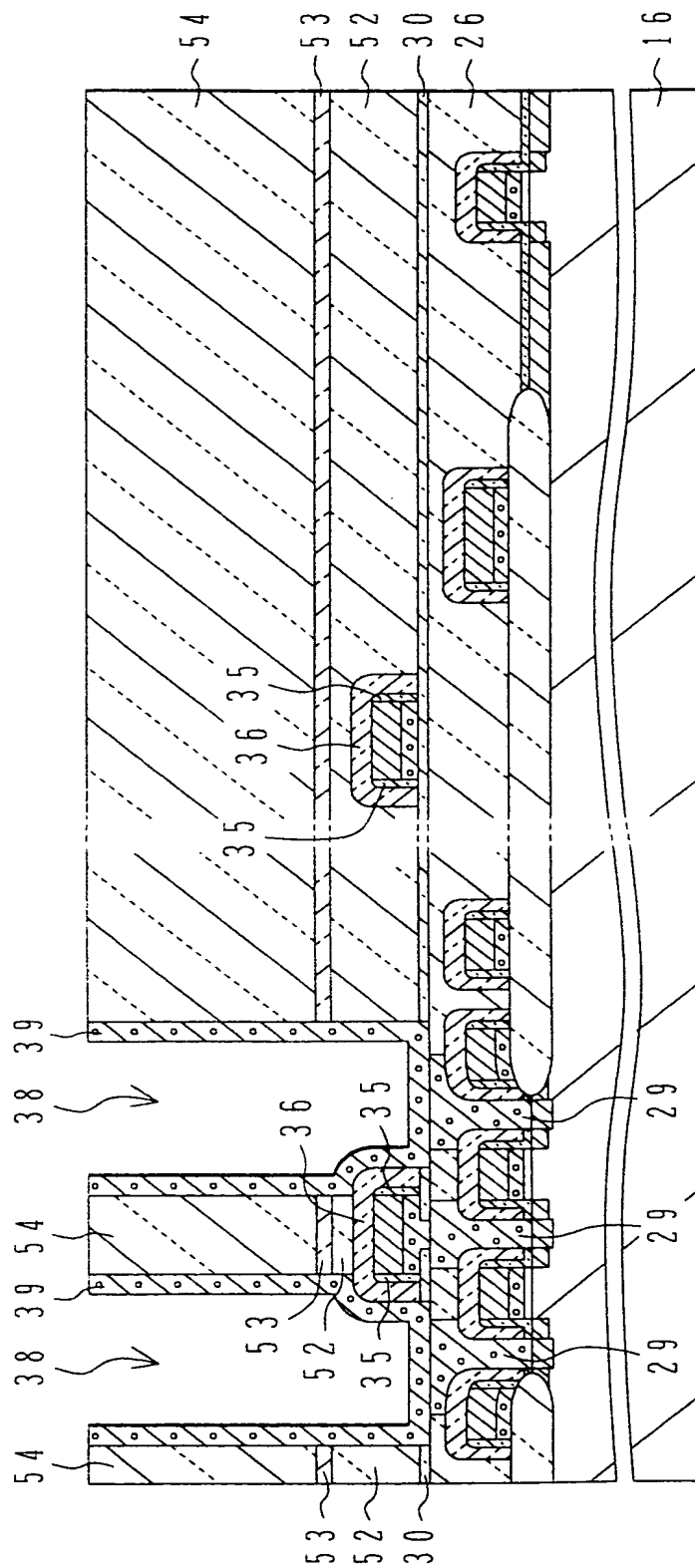


FIG. 27

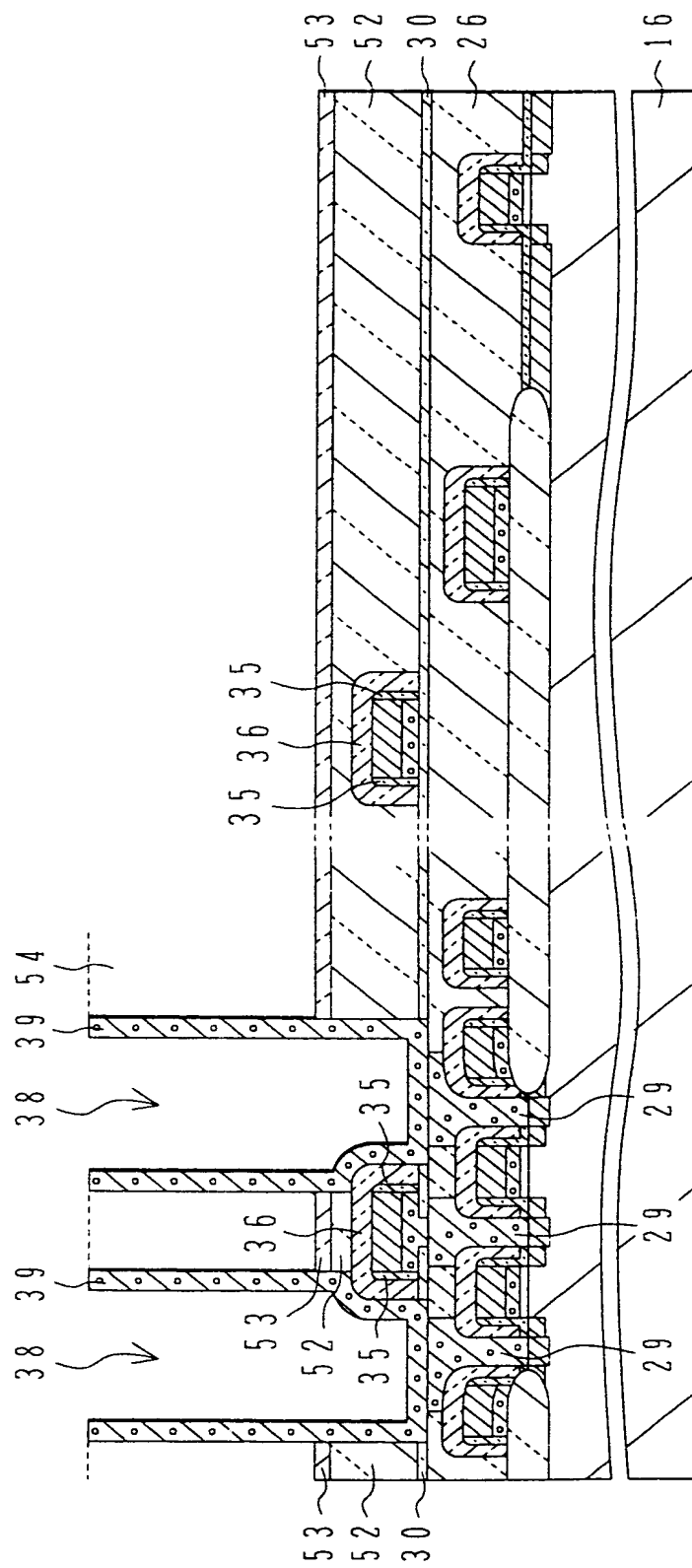
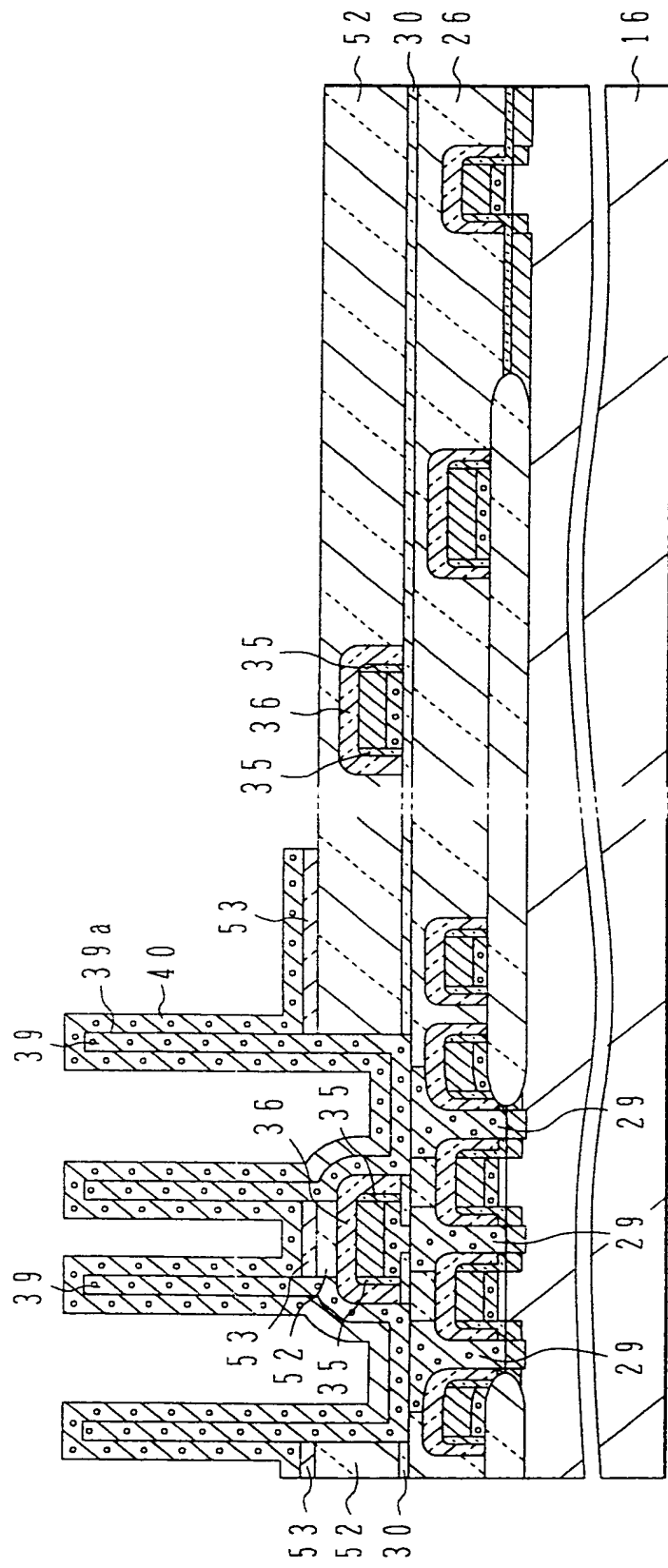


FIG.28



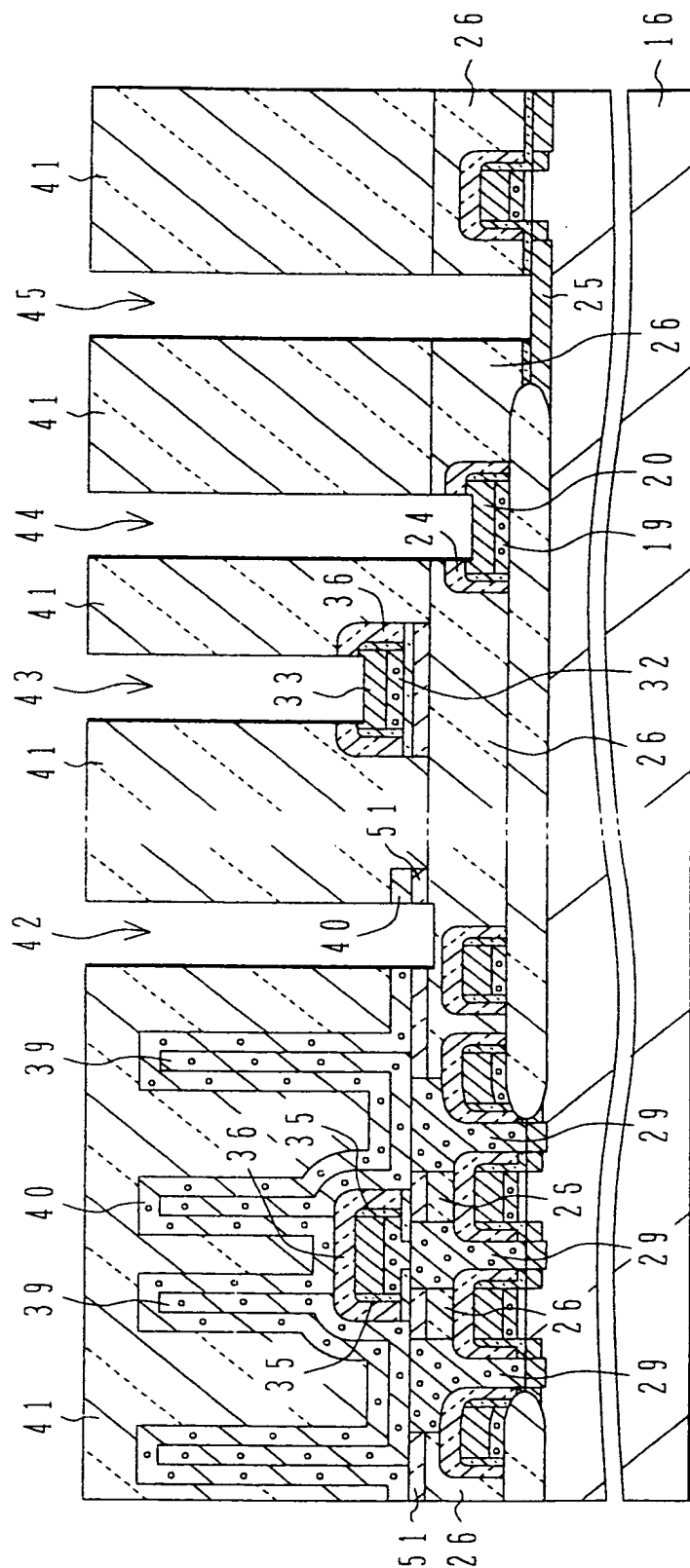


FIG.30

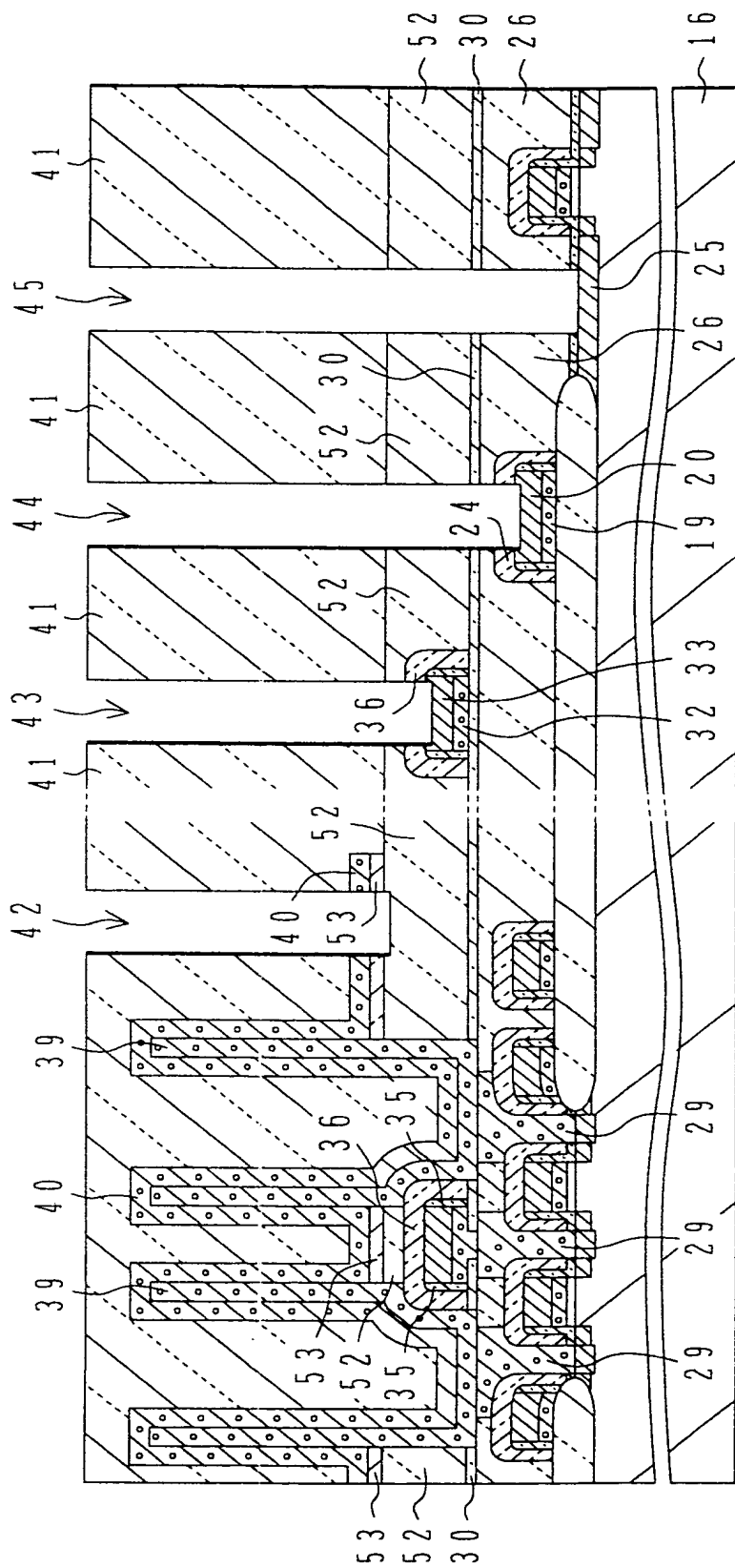


FIG.31

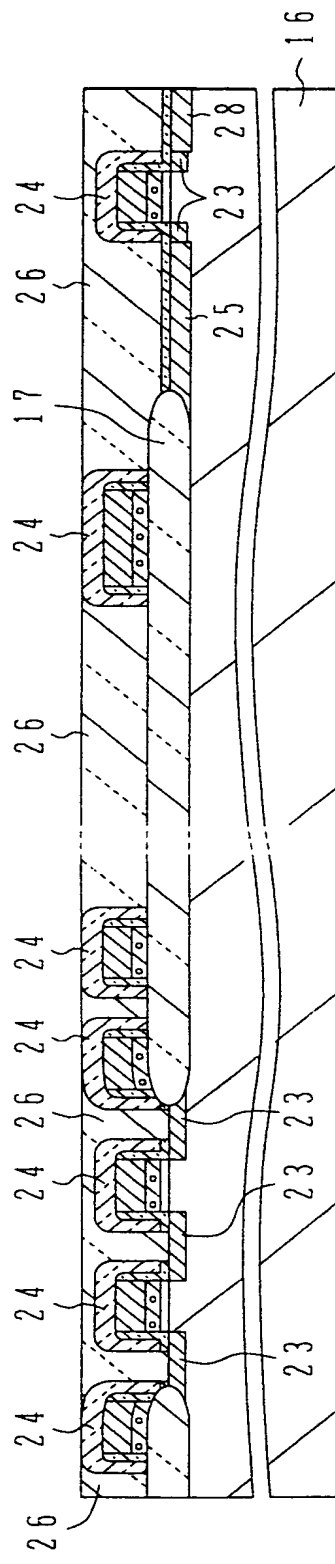


FIG. 32

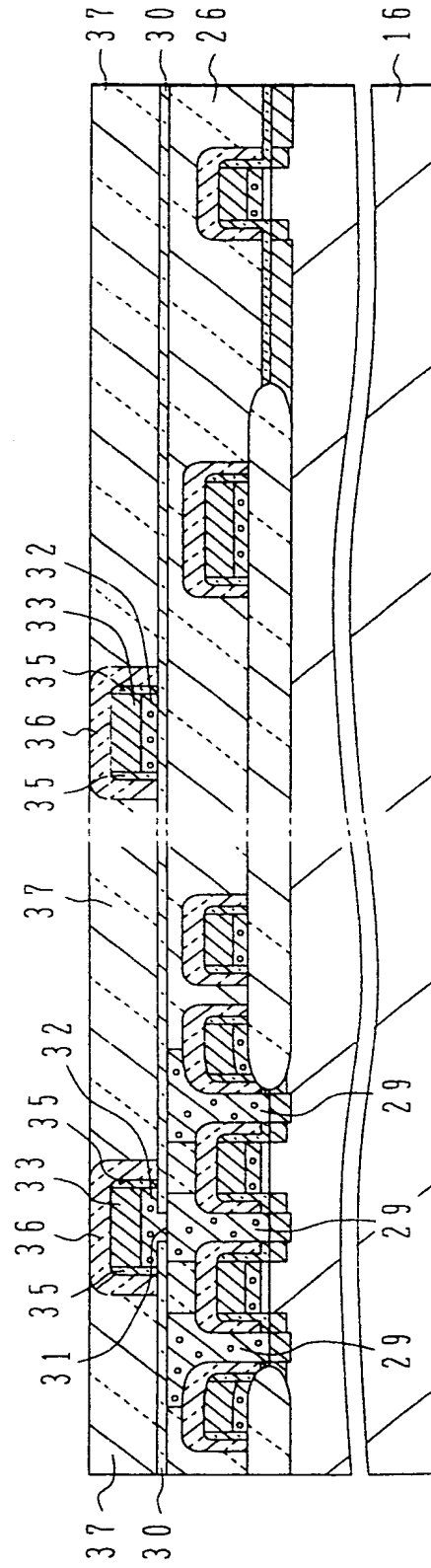


FIG. 33

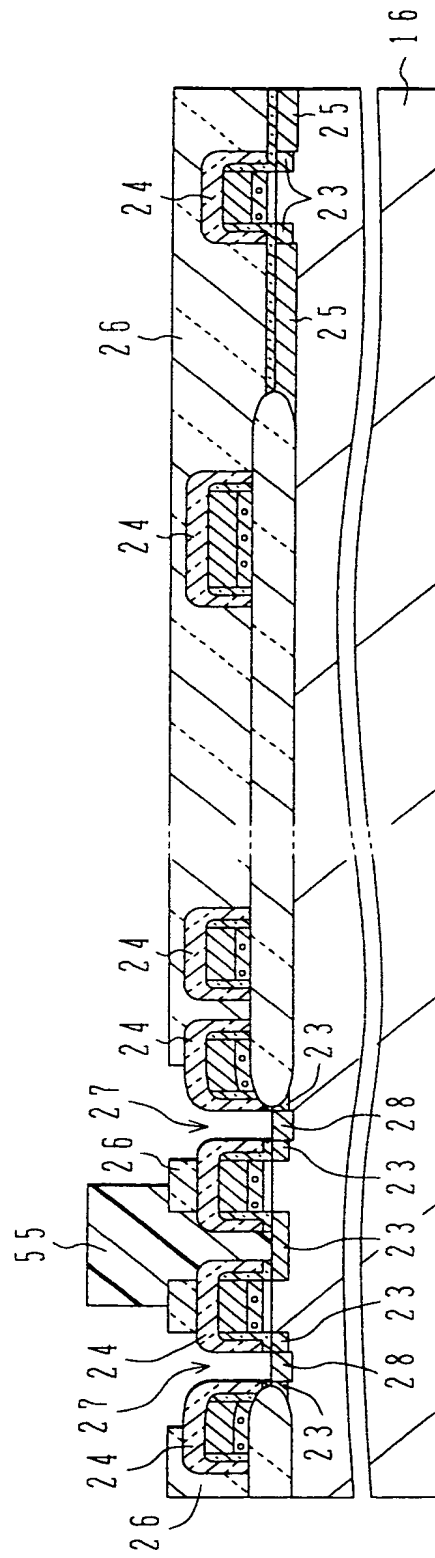


FIG.34A
PRIOR ART

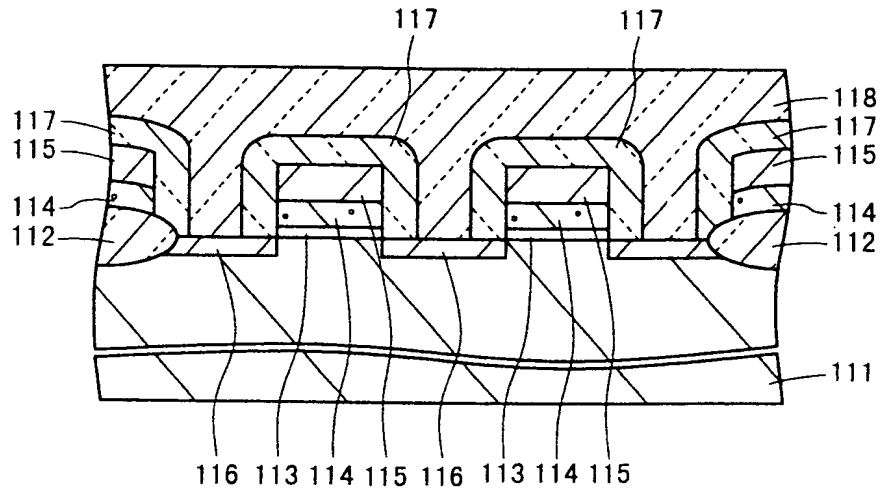


FIG.34B
PRIOR ART

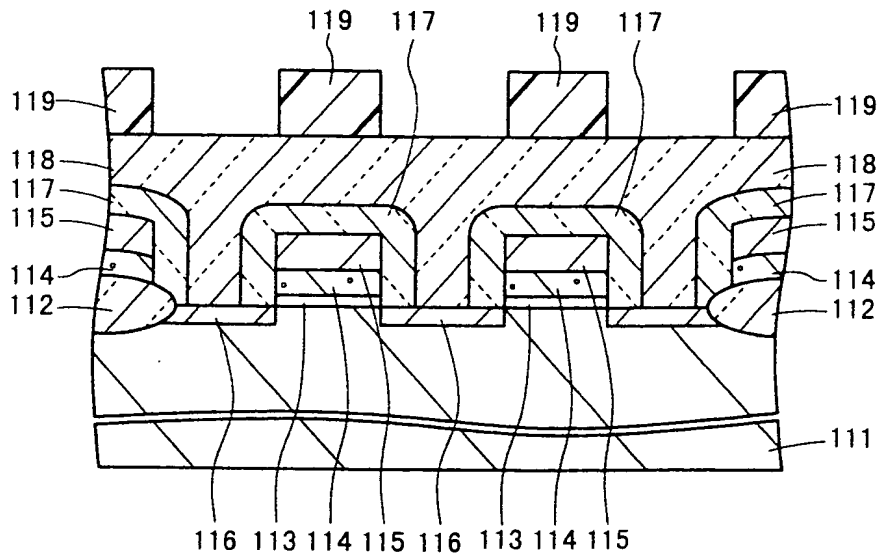


FIG.35A
PRIOR ART

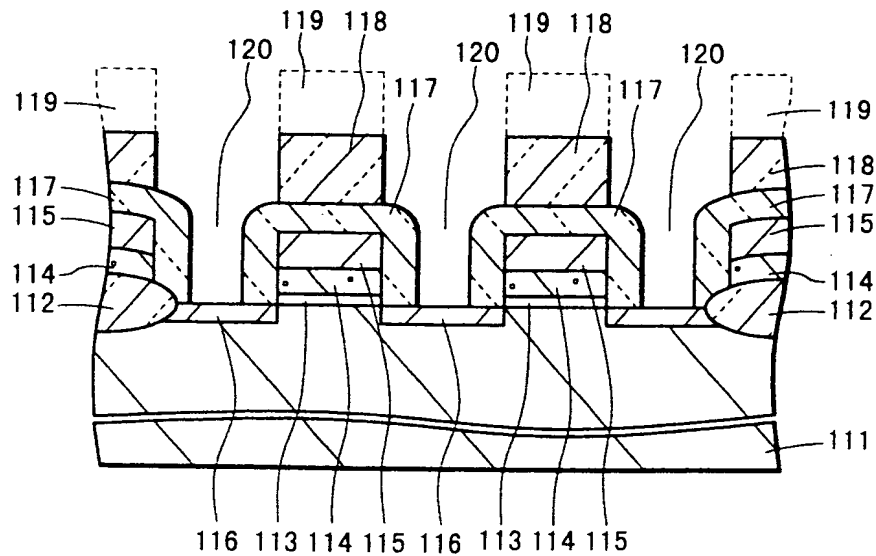


FIG.35B
PRIOR ART

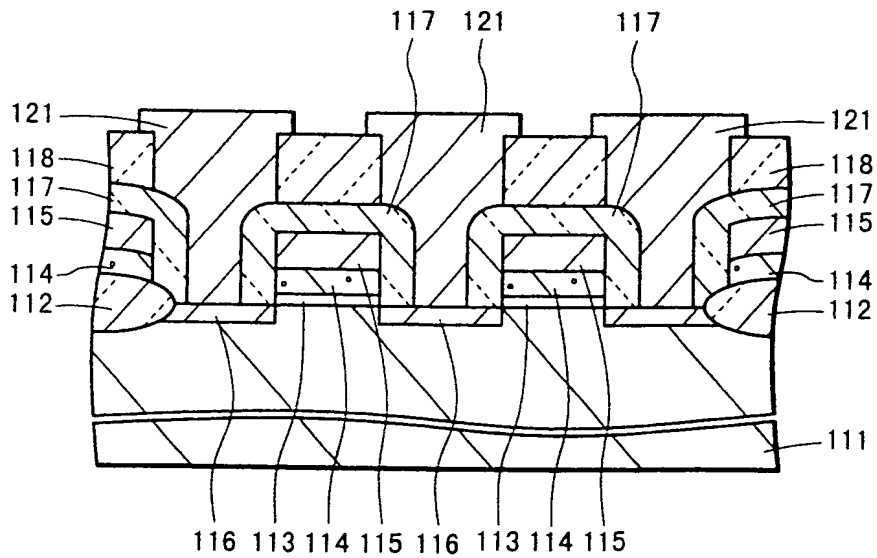


FIG.36A
PRIOR ART

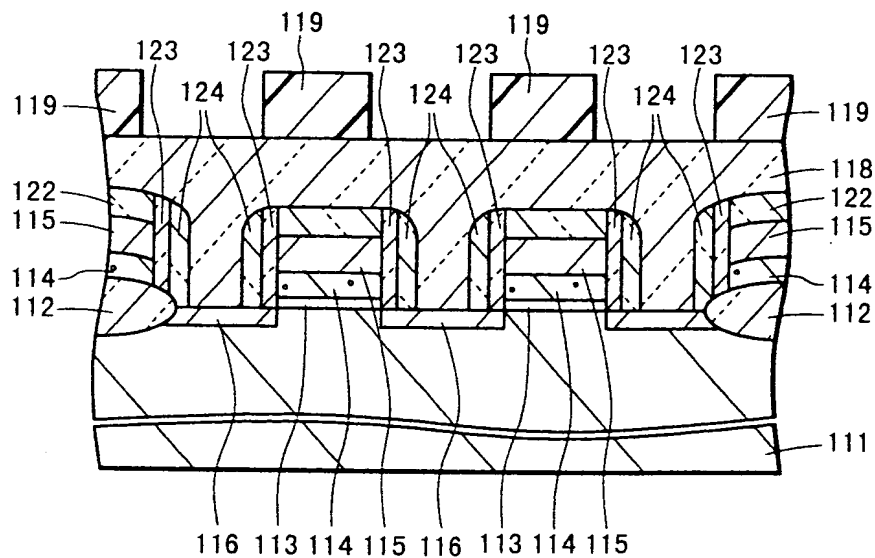


FIG.36B
PRIOR ART

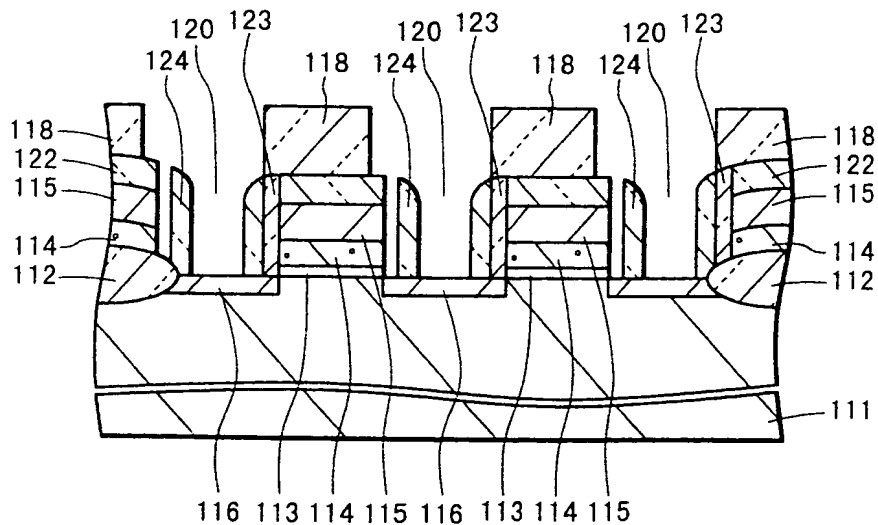


FIG.37
PRIOR ART

